



CJC

Patent

Attorney Docket No.: Intel 2207/1012302

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Sailesh KOTTAPALLI et al.

SERIAL NO. : 10/804,865

FILED : March 19, 2004

PATENT NO. : 7,149,881 B2

ISSUED : December 12, 2006

FOR : METHOD AND APPARATUS FOR IMPROVING DISPERSAL  
PERFORMANCE IN A PROCESSOR THROUGH THE USE OF  
NO-OP PORTS

GROUP ART UNIT : 2181

EXAMINER : William M. TREAT

**Certificate**

APR 04 2007

**of Correction**

M/S: CERTIFICATE OF  
CORRECTION BRANCH  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: CERTIFICATE OF CORRECTION BRANCH, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: <u>March 27, 2007</u>	Signature <u>Monique Cruz</u> Monique Cruz

**REQUEST FOR CERTIFICATE OF CORRECTION**

Dear Sir:

We have compared the above patent with the application as filed and have found errors in the printing of the patent. We respectfully request that the enclosed Certificate of Correction on Form PTO-1050 be issued correcting the mistakes set forth therein under authority of 35 U.S.C. §254. The exact column and line number where the errors occurred in the patent are listed on the enclosed certificate.

APR 04 2007

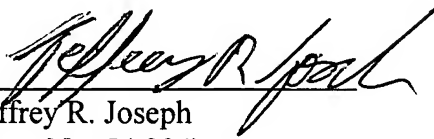
REQUEST FOR CERTIFICATE OF CORRECTION  
U.S. Patent No. 7,149,881 B2

The Office is hereby authorized to charge any additional fees, or credit any overpayments,  
to Kenyon & Kenyon LLP, Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON LLP

Dated: March 27, 2007

By:   
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APR 04 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO : 7,149,881 B2

DATED : December 12, 2006

INVENTOR(S) : Sailesh KOTTAPALLI et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 36: "No-op"  
should be --non-execution--

Column 4, line 37: "No-op"  
should be --non-execution--

Column 4, line 38: "process NOP instructions of said plurality of  
instructions"  
should be --process instructions to take no action for  
an instruction cycle--

Column 4, line 47-48: "No-op port coupled to said dispersal logic, said  
No-op port to process NOP instructions of said  
plurality of instructions"  
should be --a non-execution port coupled to said dispersal logic,  
said non-execution port to process instructions to take  
no action for an instruction cycle--

Column 4, line 61: "claim 6"  
should be --claim 5--

Column 4, line 65: "claim 7"  
should be --claim 6--

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PATENT NO. 7,149,881 B2

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,149,881 B2  
 DATED : December 12, 2006  
 INVENTOR(S) : Sailesh KOTTAPALLI et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 67: "No-op"  
 should be --non-execution--

Column 5, line 1: "claim 8"  
 should be --claim 7--

Column 5, line 3: "No-op"  
 should be --non-execution--

Column 5, line 6: "No-op"  
 should be --non-execution--

Column 5, lines 7-9: " determining if an instruction is a NOP instruction; and  
 dispersing said NOP instruction for execution by a No-op  
 port"  
 should be --determining if an instruction is an instruction to take  
 no action for an instruction cycle; and  
 dispersing said instruction to take no action for an  
 instruction cycle for execution by a non-execution port--

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